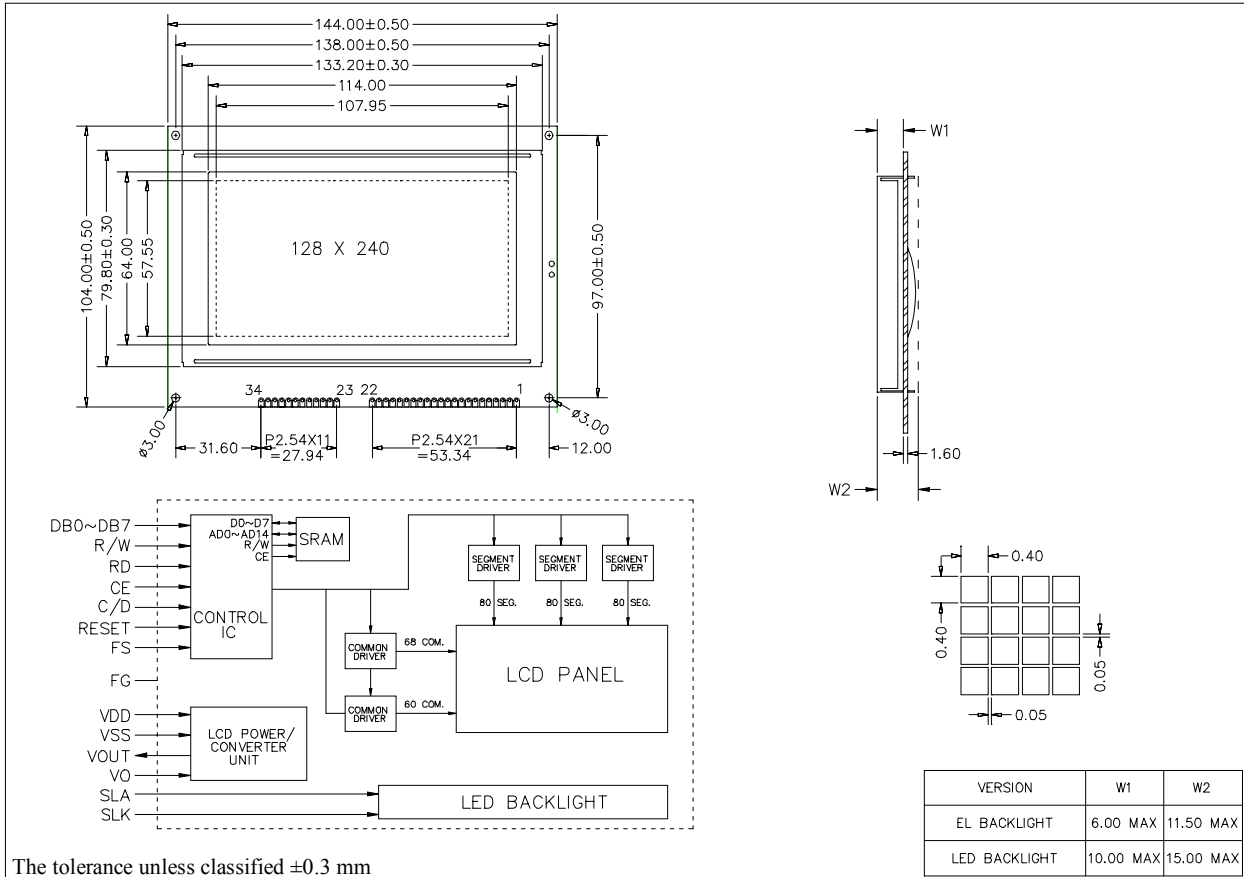


STANDARD GRAPHIC MODULES

DG240128A OUTLINE DIMENSION AND BLOCK DIAGRAM



The tolerance unless classified ± 0.3 mm

PIN ASSIGNMENT		
Pin No.	Symbol	Function
1	FG	Frame Ground
2	VSS	Ground Terminal
3	VDD	Supply Terminal
4	V0	Power Supply for LCD Driver
5	WR	Data Write
6	RD	Data Read
7	CE	Chip Enable
8	C/D	H : Instruction, L : Data
9	NC	-
10	RST	Reset Signal
11	DB0	Data Bus Line
12	DB1	Data Bus Line
13	DB2	Data Bus Line
14	DB3	Data Bus Line
15	DB4	Data Bus Line
16	DB5	Data Bus Line
17	DB6	Data Bus Line

PIN ASSIGNMENT		
Pin No.	Symbol	Function
18	DB7	Data Bus Line
19	FS	Font Selection
20	VOUT	Output Voltage
21	SLA	Led Backlight Power Supply
22	SLK	Led Backlight Ground
23	SLK	Led Backlight Ground
24	SLA	Led Backlight Power Supply
25	VOUT	Output Voltage
26	VO	Power Supply for LCD Driver
27	VSS	Ground Terminal
28	VDD	Supply Terminal
29	NC	-
30	HSCP	Shift Clock Pulse
31	LP	Latch Pulse
32	FR	Frame Signal
33	CDATA	Synchronous Signal for Row
34	ED	Data Input for Columns Driver

ABSOLUTE MAXIMUM RATING					
Item	Symbol	Conditions	Min.	Max.	Unit
Power Supply Voltage	Vdd-Vss	-	-0.3	7.0	V
Input Voltage	Vin	-	-0.3	Vdd + 0.3	V
Operation Temperature	Topr	Nor	0	50	°C
Storage Temperature	Tstg	Nor	-20	70	°C

ELECTRICAL CHARACTERISTIC (Vdd = + 5v, Ta = 25°C)						
Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Logic Supply Voltage	Vdd	-	4.5	5	5.5	V
"H" Input Voltage	V _{in}	-	Vdd-2.2	-	Vdd	V
"L" Input Voltage	V _n	-	0	-	0.8	V
"H" Output Voltage	V _{out}	-	Vdd - 0.3	-	Vdd	V
"L" Output Voltage	V _{ck}	-	0	-	0.3	V
Module Supply Current	I _{dd}	-	-	40.0	50.0	mA
LCD Driving Voltage	V _{lcd}	Vdd-Vo	-	17.5	-	V